

IN THE CLAIMS

For the convenience of the Examiner, all pending claims of the present Application are shown below in numerical order whether or not an amendment has been made and applying the revised amendment practice of 37 CFR 1.121 – IFW Final Rule.

1. **(Original)** A server processing card, comprising:
a printed circuit board;
a central processing unit coupled with the printed circuit board;
memory integrated circuits coupled with the printed circuit board;
network interface integrated circuitry coupled with the printed circuit board; and
a master control module coupled with the central processing unit, the master control module being operable to monitor a master signal input in order to detect the presence of a master control signal being communicated to the server processing card; and
wherein the master control module remains idle if the master control signal is not detected.
2. **(Original)** The server processing card of Claim 1, wherein the master control module becomes active if the master control signal is detected.
3. **(Original)** The server processing card of Claim 2, wherein the active master control module performs hardware master responsibilities with regard to a plurality of computing devices coupled with the server processing card within a server chassis.
4. **(Original)** The server processing card of Claim 3, wherein the active master control module monitors health information regarding hardware components coupled with the server processing card within the server chassis, the hardware components including the plurality of computing devices.

5. **(Original)** The server processing card of Claim 4, wherein the health information regarding the hardware components is selected from the group consisting of operating temperature, operating voltages, operating fan speed, and operating disk drive health and configuration.

6. **(Original)** The server processing card of Claim 3, wherein the active master control module monitors configuration information regarding hardware components coupled with the server processing card, the hardware components including the plurality of computing devices.

7. **(Original)** The server processing card of Claim 6, wherein the configuration information is selected from the group consisting of size of a disk drive, speed of a processor, unique identification numbers associated with the hardware components, memory capacity of memory integrated circuits and operating capacity of power supplies.

8. **(Original)** The server processing card of Claim 3, wherein the active master control module monitors information regarding hardware components coupled with the server chassis, the information being selected from the group consisting of health information and configuration information.

9. **(Original)** The server processing card of Claim 8, wherein the hardware components are selected from the group consisting of power supplies, disk drives, memory integrated circuits, and cooling fans.

10. **(Original)** The server processing card of Claim 3, wherein the active master control module includes the ability to reboot at least one of the computing devices.

11. **(Original)** The server processing card of Claim 3, wherein the active master control module includes the ability to cause at least one of the computing devices to boot up from an operating system resident upon a remote component of a local area network.

12. **(Original)** The server processing card of Claim 3, wherein the network interface integrated circuitry is coupled with a local area network, and the active master control module is configured to report health and configuration data regarding at least one of the computing devices to a remote component of the local area network.

13. **(Original)** The server processing card of Claim 2, further comprising a communication coupling configured to receive a control bus and wherein the active master control module is operable to control hardware components coupled with the server processing card within a server chassis using the control bus.

14. **(Currently Amended)** A server chassis, comprising:
a plurality of server processing cards each having a respective master control module;
each master control module being operable to monitor an associated master signal input to detect a master control signal, and remain idle if the master control signal is not detected;

a midplane forming a communication coupling for communication among the plurality of server processing cards; and

wherein at least one of the plurality of server processing cards performs hardware master responsibilities with regard to at least a subset of the server processing cards.

15. **(Currently Amended)** A method for monitoring a plurality of server processing cards of a server chassis, comprising:

selecting at least one hardware master from among a plurality of server processing ~~cards,~~ cards;

transmitting a hardware master control signal to the hardware master;

activating a master control module coupled with the hardware master in response to receiving the master control signal; and

monitoring operating information regarding at least a subset of the plurality of server processing cards, from the hardware master.

16. **(Original)** The method of Claim 15, wherein the information is selected from the group consisting of health information and configuration information regarding the operation of the subset of the plurality of server processing cards.

17. **(Original)** The method of Claim 15, further comprising at least partially controlling the operation of the subset of the plurality of server processing cards, from the hardware master.

18. **(Original)** The method of Claim 15, further comprising transmitting, from the hardware master, a command to at least one of the subset of the plurality of server processing cards, the command being operable to cause the at least one of the subset to reboot.

19. **(Original)** The method of Claim 15, further comprising transmitting, from the hardware master, a command to at least one of the subset of the plurality of server processing cards, the command being operable to cause the at least one of the subset to reboot from an operating system resident upon a remote module of a local area network coupled with the plurality of server processing cards.

20. **(Currently Amended)** A computer readable medium encoded with logic operable to:

select at least one hardware master from among a plurality of server processing cards coupled with a server chassis;

transmit a hardware master control signal to the hardware master;

activate a master control module coupled with the hardware master **in response to receiving the master control signal**; and

monitor operating information regarding at least a subset of the plurality of server processing cards, from the hardware master.

21. **(Original)** The computer readable medium of Claim 20, wherein the logic is further operable to at least partially control the operation of the subset of the plurality of server processing cards, from the hardware master.

22. **(Original)** The computer readable medium of Claim 20, wherein the logic is further operable to transmit, from the hardware master, a command to at least one of the subset of the plurality of server processing cards, the command being operable to cause the at least one of the subset to reboot.

23. **(Original)** The computer readable medium of Claim 20, wherein the logic is further operable to transmit, from the hardware master, a command to at least one of the subset of the plurality of server processing cards, the command being operable to cause the at least one of the subset to reboot from an operating system resident upon a remote module of a local area network coupled with the plurality of server processing cards.

24. **(Currently Amended)** A system, comprising:
means for selecting at least one hardware master from among a plurality of server processing cards coupled with a server chassis;
means for transmitting a hardware master control signal to the hardware master;
means for activating a master control module coupled with the hardware master in response to receiving the master control signal; and
means for monitoring operating information regarding at least a subset of the plurality of server processing cards, from the hardware master.

25. **(Original)** The system of Claim 24, further comprising means for at least partially monitoring the operation of the subset of the plurality of server processing cards, from the hardware master.

26. **(Original)** The method of Claim 24, further comprising means for transmitting, from the hardware master, a command to at least one of the subset of the plurality of server processing cards, the command being operable to cause the at least one of the subset to reboot.

27. **(Original)** The system of Claim 24, further comprising means for transmitting, from the hardware master, a command to at least one of the subset of the

plurality of server processing cards, the command being operable to cause the at least one of the subset to reboot from an operating system resident upon a remote module of a local area network coupled with the plurality of server processing cards.